

12/16/2005 14:46

4084337460 LSI LOGIC → 915712732885

NO. 917 D005

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Leo Peters
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Mark Salvatore	(Depositor's name)
<i>Mark Salvatore</i>	(Signature)
December 16, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/716,259	11/18/2003	Subramanian Ramesh	5201-27300 03-1509	6522

TITLE OF INVENTION: MEMORY CELL ARCHITECTURE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	01/09/2006
EXAMINER	ART UNIT	CLASS-SUBCLASS			
LE, THONG QUOC	2827	365-063000			

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

LSI Logic Corporation

Milpitas, California, USA

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are enclosed:

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b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature Timothy R. CrollDate 13 Dec 05Typed or printed name Timothy R. CrollRegistration No. 36,771

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